

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Peter J. Hopper

Application No.: New

Filed: Herewith

For: INTERCONNECT EXHIBITING
REDUCED PARASITIC
CAPACITANCE VARIATION

Group Art Unit: Unknown

Examiner: Unknown

**INFORMATION DISCLOSURE
STATEMENT**

121 Spear Street, Suite 290
San Francisco, CA 94105
(415) 512-1312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Label No. EV 338307726US in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 15, 2003.

STALLMAN & POLLOCK LLP

Dated: 12/15/03 By: Janet Chan
Janet Chan

Sir:

Applicant(s) submit(s) herewith patents, publications or other information [attached hereto and listed on the attached Form PTO-1449 (modified)] of which he is aware, which he believe(s) may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits, or before a first office action after filing a Request for Continued Examination under §1.114.
- (d) is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is

known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.

(e) is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and the Issue Fee has not been paid, and is accompanied by the fee (\$130) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) is checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

(f) Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

(g) No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publication(s) is set forth on the attached Form PTO-1449 (Modified).

A copy of the items on PTO-1449 (Modified) is supplied herewith, except as noted below.

Those patent(s) or publication(s) which are marked with an asterisk (*) in the attached form PTO-1449 (Modified) are not supplied because they are (a) either U.S. Patents and this is an application filed after June 30, 2003, or (b) were previously cited by or submitted to the Office in a prior application nos. 10/119,452, filed April 10, 2002, and 09/429,442 filed October 28, 1999, now U.S. Patent No. 6,414,367 and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

A concise explanation of relevance of the items listed on form PTO-1449 (Modified) is:

- (k) not given
- (l) given for each listed item
- (m) given for only non-English language listed item(s) [Required]
- (n) is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references [copy attached].

The Examiner is reminded that a "concise explanation of the relevance" of the submitted items "may be nothing more than identification of the particular figure or paragraph of the patent or publication which has some relation to the claimed invention," MPEP § 609.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

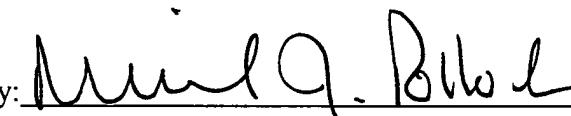
The Commissioner is hereby authorized to charge our Deposit Account No. 50-1703, under Order No. NSC1-G2511, for any fees required in connection with the filing of this Information Disclosure Statement. **A duplicate copy of this Notice is enclosed for this purpose.** In particular, in the event that an Office Action has crossed in the mail with this Information Disclosure Statement, the Commissioner is authorized to charge the above-named deposit account for any fees required pursuant to CFR §§ 1.17(p) or 1.17(i)(1).

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: 12-15-2003

By:


Michael J. Pollock
Reg. No. 29,098

Attorneys for Applicant(s)

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| INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i> | | Docket Number (Optional) NSC1-G2511 [P04437 D2] | | Application Number New | |
| | | Applicant(s) Peter J. Hopper | | | |
| | | Filing Date Herewith | | Group Art Unit Unknown | |

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | REF | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE |
|--------------------------|------------|------------------------|-------------|-------------------|--------------|-----------------|--------------------|
| | *AA | 5,128,737 | 07/07/92 | van der Have | 257 | 503 | 03/02/90 |
| | *AB | 5,635,753 | 06/1997 | Hofflinger et al. | 257 | 621 | 12/30/92 |
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| | *AD | 6,084,304 | 07/04/00 | Huang et al. | 257 | 762 | 06/05/98 |
| | *AE | 6,136,640 | 10/24/00 | Marty et al. | 438 | 239 | 07/17/98 |
| | *AF | 5,164,334 | 11/17/92 | Mizushima | 437 | 203 | 12/26/90 |
| | *AG | 2001/0002072 | 05/31/01 | Ireland | 257 | 758 | 12/15/00 |

FOREIGN PATENT DOCUMENTS

| | REF | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
|--|------------|------------------------|-------------|----------------|--------------|-----------------|--------------------|-----------|
| | | | | | | | YES | NO |
| | *AH | JP-11-352512 | 12/24/99 | Japan | G02F | 1/136 | X | |
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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

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| *AI | Stanley Wolf, "Silicon Processing for the VLSI ERA", Volume 2: Process Integration, 1990, pp. 183-186. |
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| Examiner | Date Considered |
|---|-----------------|
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |